1FW 2115

Attorney's Docket No. 5649-886

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

La re: Jung et al.

OCT 0 3 2005

erial No.: 09/941,091

Filed: August 28, 2001

For: INTEGRAT

INTEGRATED CIRCUIT MEMORY SYSTEMS HAVING PROGRAMMABLE SIGNAL BUFFERS FOR ADJUSTING SIGNAL TRANSMISSION DELAYS AND

METHODS OF OPERATING SAME

Date: September 30, 2005

Confirmation No.: 5816

Group Art Unit: 2115

Examiner: Chun Cao

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT

Sir:

Applicants provide the present Amendment to address the issues raised in the Office Action (hereinafter "the Action) mailed July 18, 2005.

Amendments to the Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Remarks begin on page 12 of this paper.

It is not believed that an extension of time and/or additional fee(s)-including fees for net addition of claims-are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.

